F-067

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 09-284078
(43)Date of publication of application : 31.10.1997

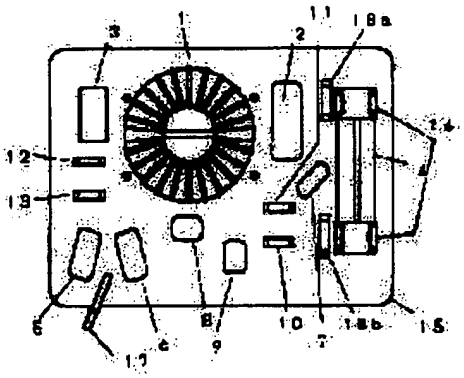
(51)Int.CI. H03H 7/09
H01F 27/29

(21)Application number : 08-092360
(22)Date of filing : 15.04.1996

(71)Applicant : MATSUSHITA ELECTRIC IND CO LTD
(72)Inventor : MIHARA MAKOTO
SUENAGA HARUO
SAKAI SHINICHI
ISHIO YOSHIAKI

(54) TERMINAL NOISE FILTER

(57)Abstract:
PROBLEM TO BE SOLVED: To reduce a temperature rise at a fuse terminal and to obtain the excellent productivity and workability in the terminal noise filter to prevent an EMC fault due to a terminal noise.
SOLUTION: The terminal noise filter uses tab terminals 10, 11 to connect an electric wire to a printed circuit board 15 as a heat dissipation means. Thus, the terminals with excellent mount performance onto the printed circuit board and a flat shape are arranged up to the vicinity of a clip 14 requiring heat dissipation. Thus, since excellent high density mount performance is attained and the heat dissipation part is concentrated onto the vicinity of the heat generating part, high thermal conductivity is attained and a fuse terminal temperature is easily reduced.



LEGAL STATUS

[Date of request for examination]	04.06.1999
[Date of sending the examiner's decision of rejection]	04.09.2001
[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]	
[Date of final disposal for application]	
[Patent number]	
[Date of registration]	

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

F-068

Analog low-pass filter with double sampling

Patent number: FR2765417
Publication date: 1998-12-31
Inventor: SHIN YUN TAE
Applicant: HYUNDAI ELECTRONICS IND (KR)
Classification:
- International: H03H11/02
- european: H03H19/00B
Application number: FR19980008317 19980630
Priority number(s): KR19970029634 19970630

Also published as:
US6107871 (A1)
KR256251 (B1)
JP11168353 (A)

Abstract of **FR2765417**

The filter (70) includes two terminals (1,2) which receive differential analog signals (Vinp,Vinn) which charge / discharge four capacitors (Cinn1,Cinn2, Cinp1, Cinp2). A differential amplifier (4) is connected to the common terminal of the capacitors. A filtering module (5) is connected between the amplifier asynchronous output and the filter output terminal. Switches (SW1SW8) control the charge /discharge sequence of the capacitors. These switches are controlled by a clock signal.